

650V 11A N-Channel Super Junction Power MOSFET

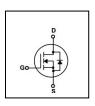
FEATURES

- RDSON \leq 0.38 Ω @Vgs=10V, Id=5.5A
- Excellent RDS(ON) and Low Gate Charge
- · Fast switching capability
- · Lead free product is acquired

Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

SYMBOL









TO-251

TO-252

TO-220F

ASSEMBLY MESSAGE

Product Name	Package	Packaging
BXC65R380U	TO-251	Tube
BXC65R380D	TO-252	Tube/Reel
BXC65R380F	TO-220F	Tube

ABSOLUTE MAXIMUM RATINGS (T_C=25°C unless otherwise noted)

Parameter		Symbol	Rating		Unit
		Symbol	BXC65R380U/D	BXC65R380F	Unit
Drain-Source Voltage		V _{DSS}	650		V
Drain Current	Continuous (T _C = 25°C)	- I _D	11		А
Drain Current	Continuous (T _C = 100°C)		7.9		А
Drain Current	Pulsed (Note1)	I _{DM}	44		А
Gate-Source Voltage		V _{GSS}	±30		V
Avalanche Energy	alanche Energy Single Pulse (Note2)		210		mJ
Avalanche Current (Note1)		I _{AR}	1.6		А
Peak Diode Recovery dv/dt		dv/dt	5		V/ns
Power Dissipation (Note	T _C =25°C	Б	118	33	W
2)	Derate above 25°C	- P _D	0.944	0.264	W/°C
Maximum Junction Temperature		TJ	150		°C
Storage Temperature Range		T _{STG}	-55 to 150		°C

Note: 1. Limited by maximum junction temperature, maximum duty cycle is 0.75

2.L=3mH, VDD=50V, RG=25 Ω , Starting TJ = 25°C

Version: 1.1



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THERMAL CHARACTERISTICS

Dovometer	Symbol	Ma	llmit	
Parameter		BXC65R380U/D	BXC65R380F	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	1.06	3.8	°C / W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	87	62	°C / W

ELECTRICAL CHARACTERISTICS (T_J=25°C,unless otherwise Noted)

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
OFF CHARACTERISTICS				•		
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V, ID=250μA	650			V
Zana Cata Malta na Buain Commant	I _{DSS}	VDS=650V, VGS=0V			1	uA
Zero Gate Voltage Drain Current		VDS=520V, TC = 125°C		10		uA
Gate-Body Leakage Current, Forward		VGS=30V			100	nA
Gate-Body Leakage Current, Reverse	I _{GSS}	VGS=-30V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	VDS=VGS, ID=250μA	2.5		4.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	VGS=10V, ID=5.5A		0.34	0.38	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	\/__\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		830		pF
Output Capacitance	Coss	VDS=100V, VGS=0V, f=1.0MHz		37		pF
Reverse Transfer Capacitance	C _{RSS}			0.65		pF
SWITCHING PARAMETERS						
Turn-ON Delay Time	t _{D(ON)}			16		ns
Turn-ON Rise Time	t _R	VDD=400V, ID=5.5A, VGS = 10V ,RG=25Ω		35		ns
Turn-OFF Delay Time	t _{D(OFF)}			75		ns
Turn-OFF Fall-Time	t _F			38		ns
Total Gate Charge(Note5)	Q _G	VDS =520V, VGS =10V, ID		19		nC
Gate Source Charge	Q _{GS}			3.2		nC
Gate Drain Charge	Q _{GD}	=5.5A		8.5		nC
SOURCE- DRAIN DIODE RATINGS	AND CHARA	ACTERISTICS				
Drain-Source Diode Forward Voltage	V _{SD}	IF=5.5A, VGS=0V		0.85		V
Diode Continuous Forward Current	Is				11	Α
Pulsed Drain-Source Current	I _{SM}				44	Α
Reverse Recovery Time	t _{RR}	VR = 400 V, IF = 5.5A		312		ns
Reverse Recovery Charge	Q _{RR}	di/dt=100 A/µs		2.9		uC



TYPICAL CHARACTERISTICS

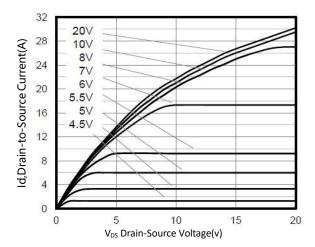


Figure 1. Typical Output Characteristics

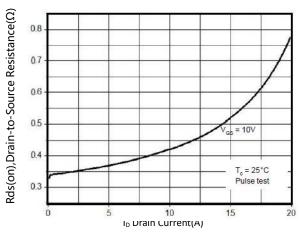


Figure 3. On-Resistance versus Drain Current

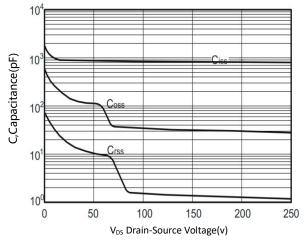


Figure 5. Typical Capacitance versus V_{DS}

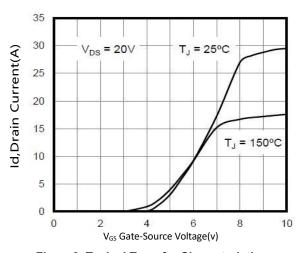


Figure 2. Typical Transfer Characteristics

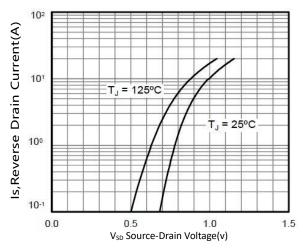


Figure 4. Diode forward voltage versus Current

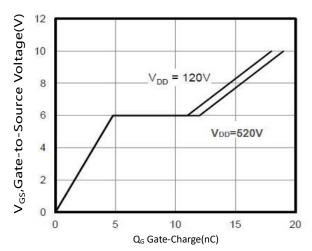


Figure 6. Typical Gate Charge versus V_{GS}



TYPICAL CHARACTERISTICS(Cont.)

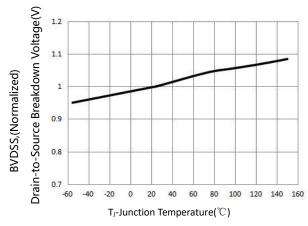


Figure 7. BV_{DSS} Variation with Temperature

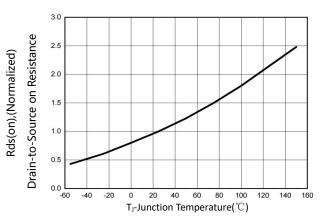


Figure8. On-Resistance Variation with Temperature

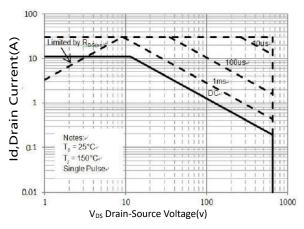


Figure 9. Maximum Safe Operating Area BXC65R380U/D

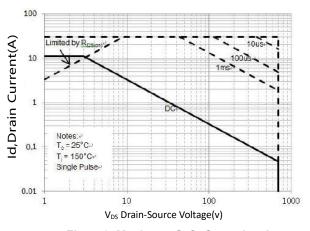


Figure 9. Maximum Safe Operating Area BXC65R380F

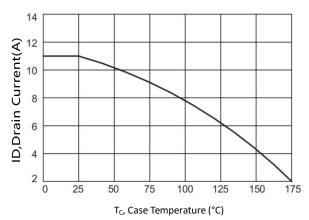
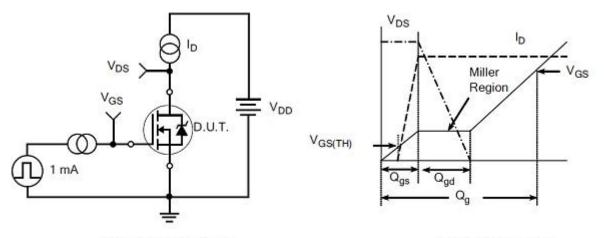


Figure 10. Maximum Continuous Drain Current versus Case Temperature

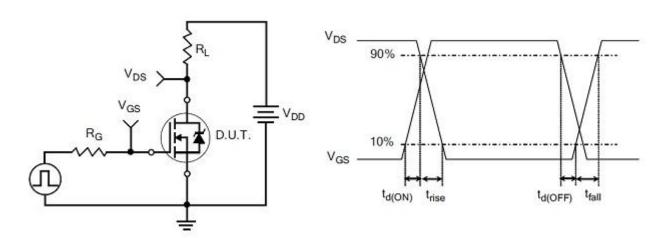


TEST CIRCUITS AND WAVEFORMS



Gate Charge Test Circuit

Gate Charge Waveform

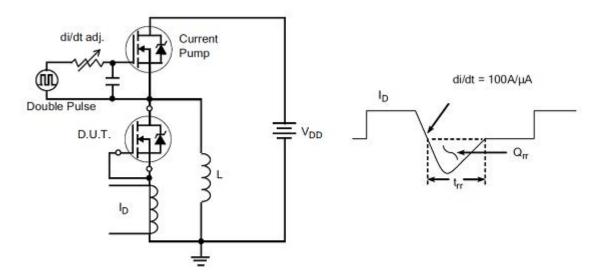


Resistive Switching Test Circuit

Resistive Switching Waveforms

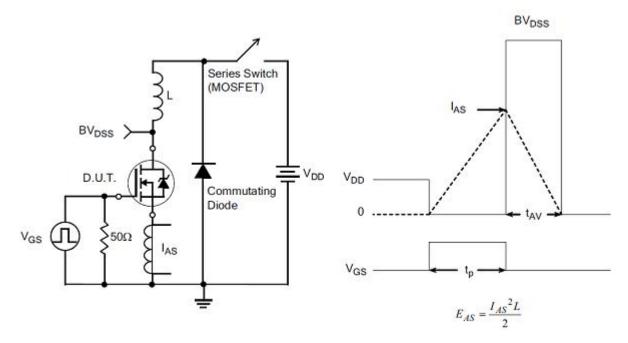


TEST CIRCUITS AND WAVEFORMS(Cont.)



Diode Reverse Recovery Test Circuit

Diode Reverse Recovery Waveform



Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms



Revision history

Document revision history

Date	Revision	Changes
10-Sep-2021	1.0	First release
6-Dec-2021	1.1	Update parameter

Version: 1.1

Bridgelux(Wuxi)Semiconductor Co., Ltd.

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