100V 136A N-Channel Enhancement Mode Power MOSFET

FEATURES

- RDSON≤4.5m Ω @Vgs=10V, Id=50A
- Advanced SGT process
- Excellent RDS(ON) and Low Gate Charge
- Lead free product is acquired

APPLICATION

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

Version: 1.1

SYMBOL





ASSEMBLY MESSAGE

Product Name	Package	Packaging		
BXS045N10C	PDFN5*6	Reel		

ABSOLUTE MAXIMUM RATINGS (T_C=25°C unless otherwise noted)

Parameter		Symbol	Rating	Unit	
			PDFN5*6		
Drain-Source Voltage		V _{DSS}	100	V	
Drain Current	Cont	inuous (T _C = 25°C)	I_	136	Α
Drain Current	Cont	inuous (T _C = 100°C)	l _D	117	Α
Drain Current	Pulsed (Note1)		I _{DM}	600	Α
Gate-Source Voltage		Source Voltage		±20	V
Power Dissipation		T _C =25°C	P _D	167	W
Maximum Junction Temperature		num Junction Temperature		150	°C
Storage Temperature Range		T _{STG}	-55 to 150	°C	

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

THERMAL CHARACTERISTICS

Parameter	Cumbal	Max.	Unit	
Parameter	Symbol	PDFN5*6		
Thermal Resistance, Junction-to-Case	R _{θJC}	0.75	°C/W	



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$\textbf{ELECTRICAL CHARACTERISTICS} \ \, (T_J = 25 ^{\circ} \! \text{C}, unless \ otherwise \ Noted})$

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
OFF CHARACTERISTICS					•	1
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V, ID=250µA	100			V
Zero Gate Voltage Drain Current	I _{DSS}	VDS=100V, VGS=0V			1	uA
Gate-Body Leakage Current, Forward		VGS=20V			100	nA
Gate-Body Leakage Current, Reverse	I _{GSS}	VGS=-20V			-100	nA
ON CHARACTERISTICS			•	•		
Gate Threshold Voltage	$V_{GS(TH)}$	VDS=VGS, ID=250µA	1.2	1.8	2.5	V
	_	VGS=10V, ID=50A		4	4.5	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	VGS=4.5V, ID=30A		6.5	7.5	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	VDS=50V, VGS=0V,		3454		pF
Output Capacitance	Coss			1545		pF
Reverse Transfer Capacitance	C _{RSS}	rss f=1.0MHz		76		pF
SWITCHING PARAMETERS						
Turn-ON Delay Time	t _{D(ON)}			14.5		ns
Turn-ON Rise Time	t _R	VDD=50V, ID=50A, VGS =		20.1		ns
Turn-OFF Delay Time	t _{D(OFF)}	10V, RG=3Ω		58		ns
Turn-OFF Fall-Time	t _F			31.8		ns
Total Gate Charge(Note3)	Q_G	VDC -50V VCC -40V ID		75		nC
Gate Source Charge	Q _{GS}	VDS =50V, VGS =10V, ID =50A		15		nC
Gate Drain Charge	Q _{GD}	- JOUA		23		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage	V _{SD}	IS=50A, VGS=0V		0.8	1.2	V
Diode Continuous Forward Current	ls				136	Α

Note: 2. Pulse Test : Pulse width ≤ 300µs, Duty cycle ≤ 2%

3. Essentially independent of operating temperature



TYPICAL CHARACTERISTICS

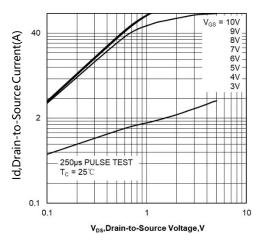


Figure 1. Typical Output Characteristics

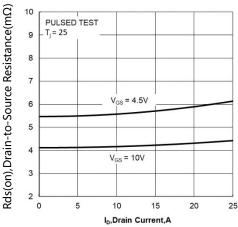


Figure 3. On-Resistance versus Drain Current

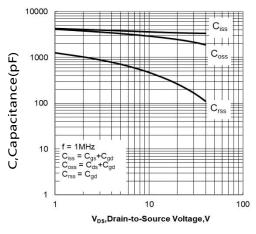


Figure 5. Typical Capacitance versus V_{DS}

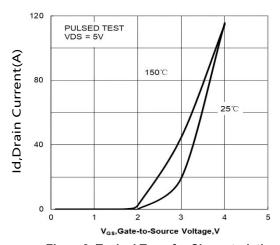


Figure 2. Typical Transfer Characteristics

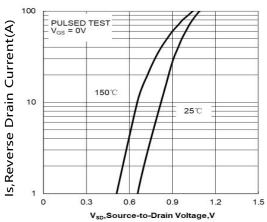


Figure 4. Diode forward voltage versus Current

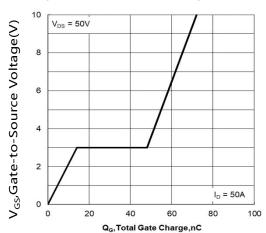


Figure 6. Typical Gate Charge versus V_{GS}

TYPICAL CHARACTERISTICS(Cont.)

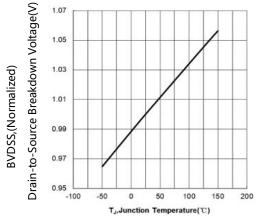


Figure 7. BV_{DSS} Variation with Temperature

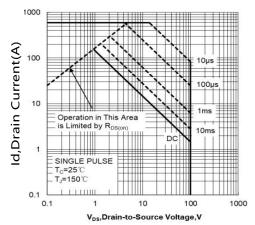


Figure 9. Maximum Safe Operating Area

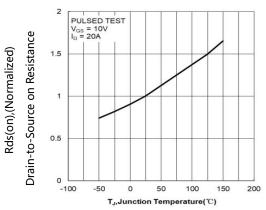


Figure 8. On-Resistance Variation with Temperature

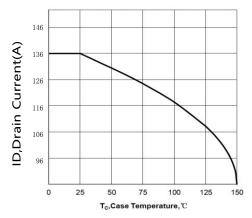
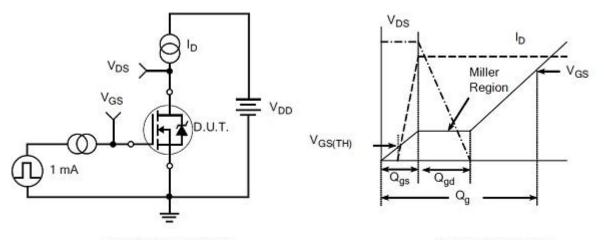


Figure 10. Maximum Continuous Drain Current versus Case Temperature

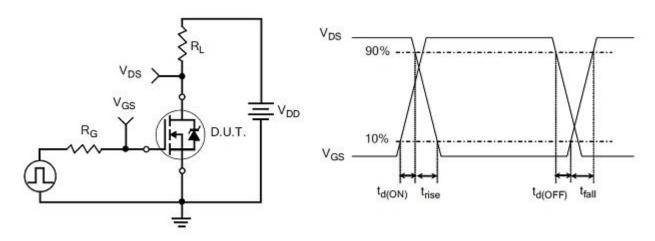


TEST CIRCUITS AND WAVEFORMS





Gate Charge Waveform

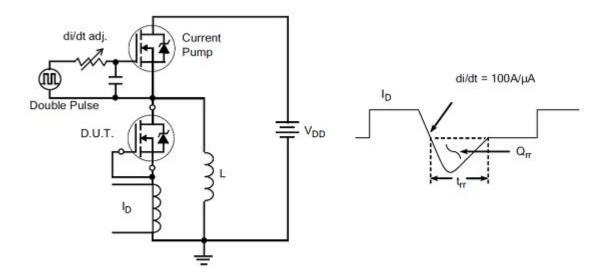


Resistive Switching Test Circuit

Resistive Switching Waveforms

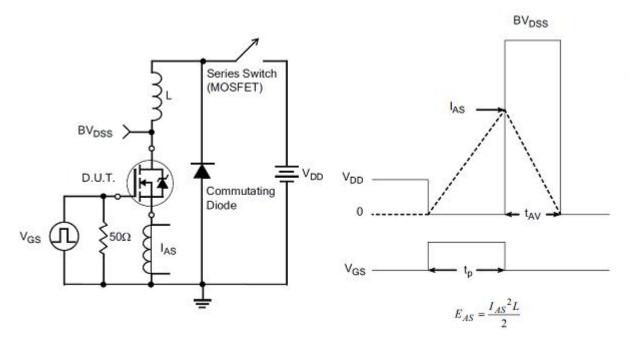


TEST CIRCUITS AND WAVEFORMS(Cont.)



Diode Reverse Recovery Test Circuit

Diode Reverse Recovery Waveform



Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

Revision history

Document revision history

Date	Revision	Changes
15-Sep-2021	1.0	First release
9-Oct-2021	1.1	Update layout format

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