

100V 65A N-Channel Enhancement Mode Power MOSFET

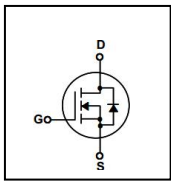
FEATURES

- $R_{DS(ON)} \leq 10.5m\ \Omega$ @ $V_{GS}=10V, I_D=20A$
- Advanced SGT process
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead free product is acquired

APPLICATION

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

SYMBOL



ASSEMBLY MESSAGE

Product Name	Marking	Package	Packaging
BXS105N10P	BX105N10P	TO-220	Tube

ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Rating	Unit	
		TO-220		
Drain-Source Voltage	V_{DSS}	100	V	
Drain Current	I_D	Continuous ($T_C = 25^\circ C$)	65	A
		Continuous ($T_C = 100^\circ C$)	45	A
Drain Current	I_{DM}	Pulsed (Note1)	260	A
Gate-Source Voltage	V_{GSS}	± 20	V	
Power Dissipation	$T_C = 25^\circ C$	P_D	100	W
Maximum Junction Temperature	T_J	150	$^\circ C$	
Storage Temperature Range	T_{STG}	-55 to 150	$^\circ C$	

Note1. Repetitive Rating: Pulse width limited by maximum junction temperature

THERMAL CHARACTERISTICS

Parameter	Symbol	Max.	Unit
		TO-220	
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.25	$^{\circ}\text{C} / \text{W}$

ELECTRICAL CHARACTERISTICS ($T_J=25^{\circ}\text{C}$, unless otherwise Noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$			1	μA
Gate-Body Leakage Current, Forward	I_{GSS}	$V_{GS}=20V$			100	nA
Gate-Body Leakage Current, Reverse		$V_{GS}=-20V$			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	1.8	2.6	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=20A$		8.8	10.5	$m\Omega$
		$V_{GS}=4.5V, I_D=10A$		11	14.5	$m\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{DS}=50V, V_{GS}=0V,$ $f=1.0MHz$		2155		pF
Output Capacitance	C_{OSS}			288		pF
Reverse Transfer Capacitance	C_{RSS}			20		pF
SWITCHING PARAMETERS						
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD}=50V, I_D=32.5A, V_{GS}$ $=10V, R_G=1.6\Omega$		13		ns
Turn-ON Rise Time	t_R			10		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			31		ns
Turn-OFF Fall-Time	t_F			8		ns
Total Gate Charge(Note2)	Q_G	$V_{DS}=50V, V_{GS}=10V, I_D$ $=32.5A$		50		nC
Gate Source Charge	Q_{GS}			15		nC
Gate Drain Charge	Q_{GD}			17		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=32.5A, V_{GS}=0V$			1.2	V
Diode Continuous Forward Current	I_S				65	A
Maximum Pulsed Drain to Source Diode Forward Current	I_{SM}				260	A

Note2. Essentially independent of operating temperature

TYPICAL CHARACTERISTICS

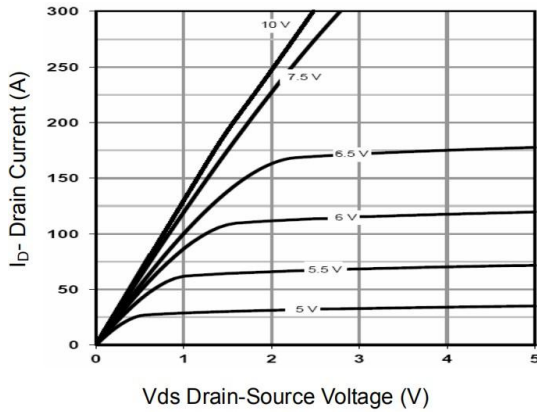


Figure 1 Output Characteristics

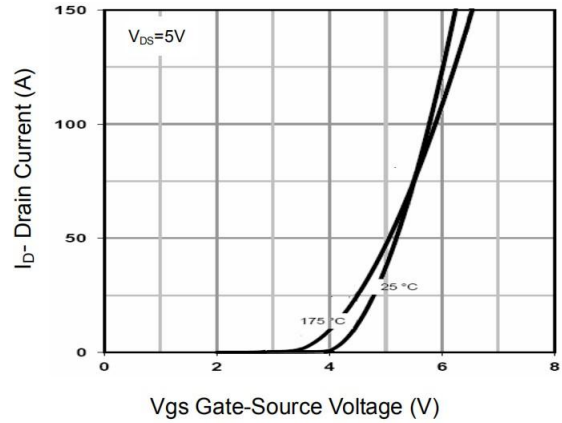


Figure 2 Transfer Characteristics

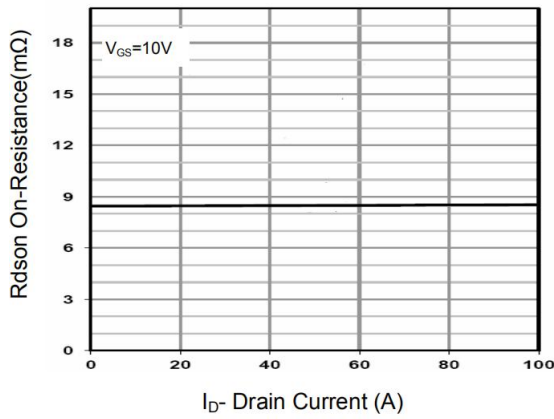


Figure 3 Rdson- Drain Current

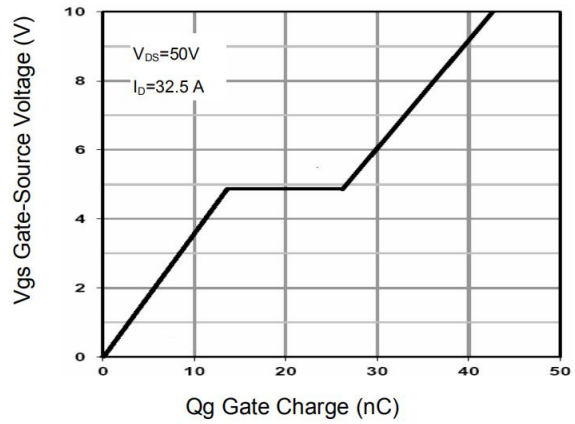


Figure 4 Gate Charge

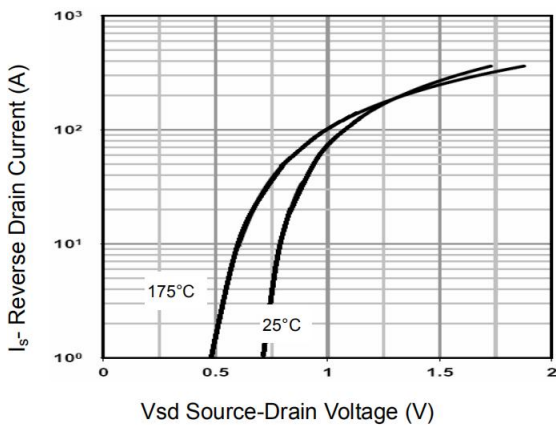


Figure 5 Source- Drain Diode Forward

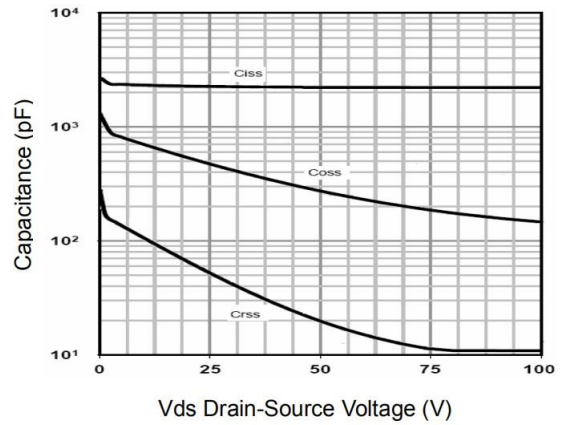


Figure 6 Capacitance vs Vds

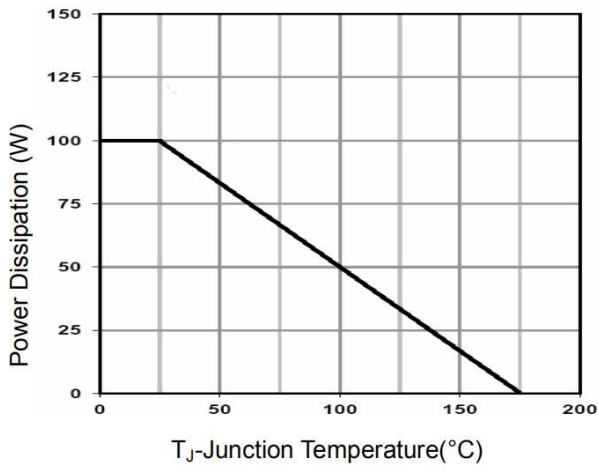


Figure 7 Power De-rating

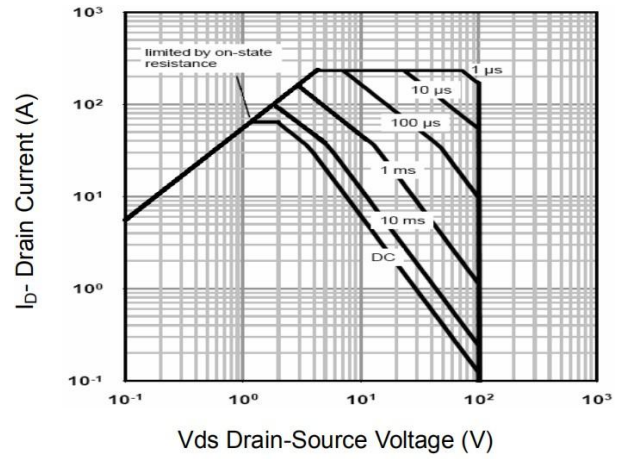


Figure 8 Safe Operation Area

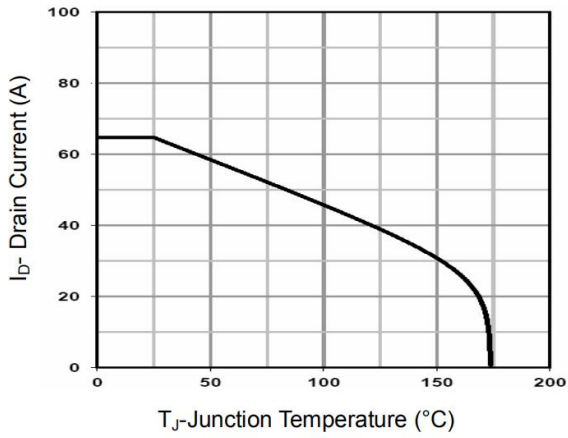


Figure 9 Current De-rating

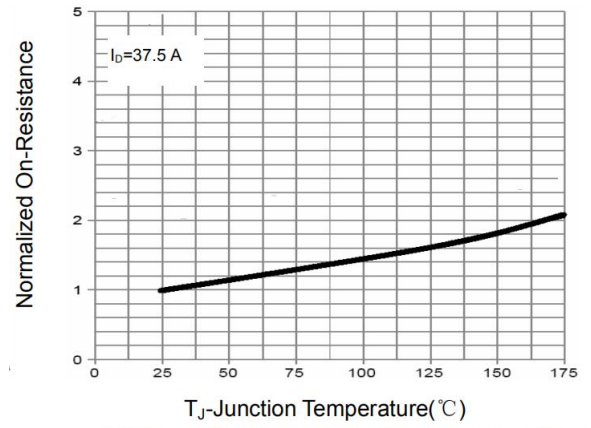
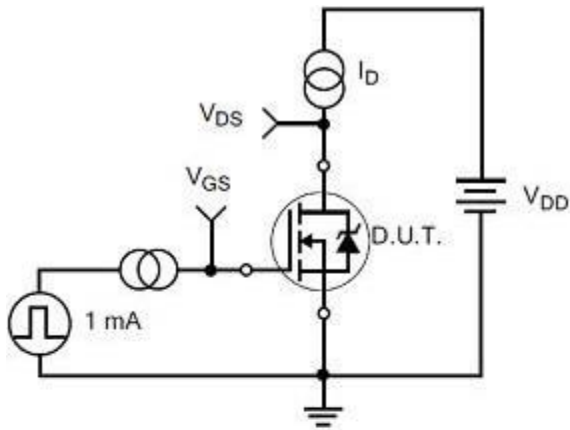
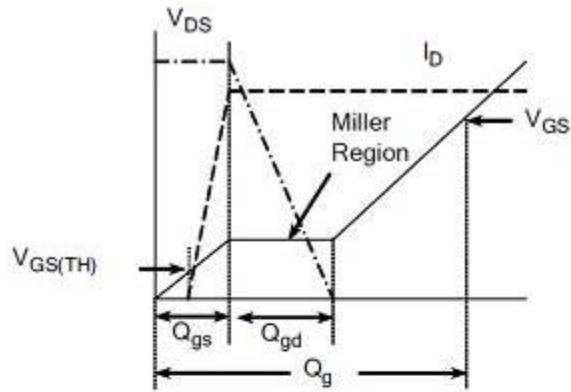


Figure 10 R_{ds(on)}-Junction Temperature

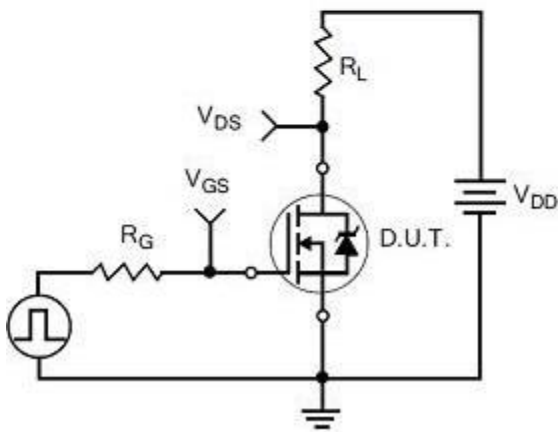
TEST CIRCUITS AND WAVEFORMS



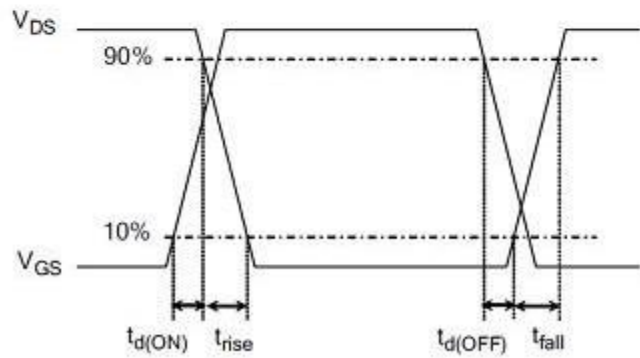
Gate Charge Test Circuit



Gate Charge Waveform

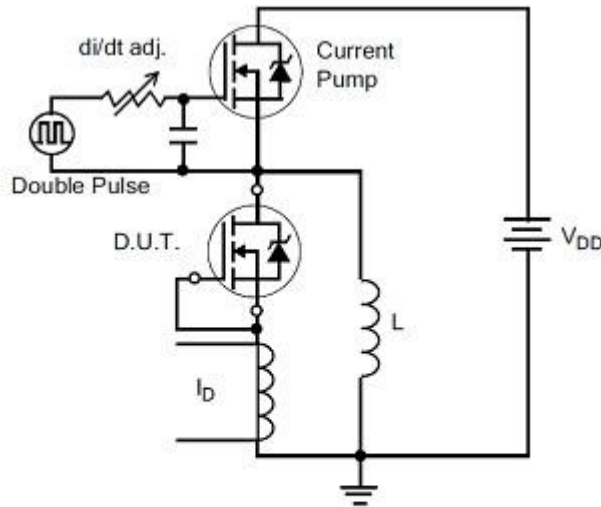


Resistive Switching Test Circuit

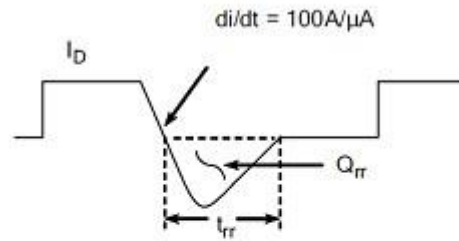


Resistive Switching Waveforms

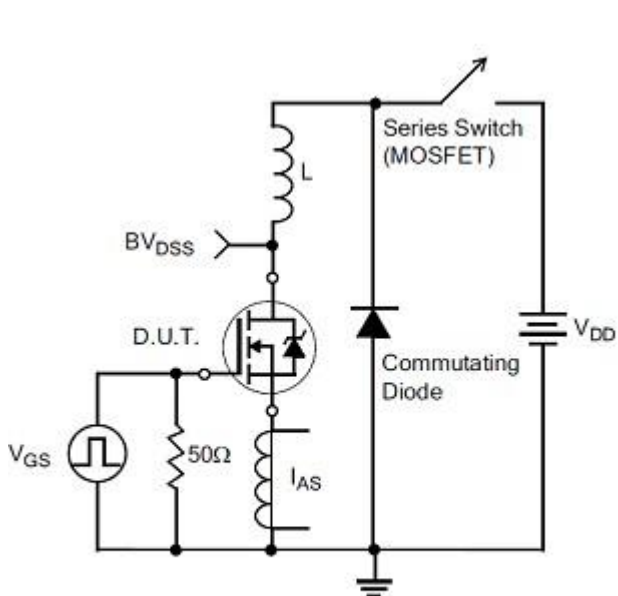
TEST CIRCUITS AND WAVEFORMS(Cont.)



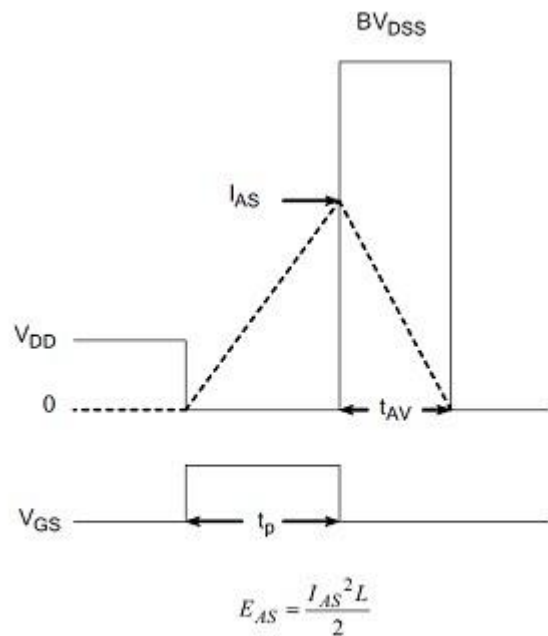
Diode Reverse Recovery Test Circuit



Diode Reverse Recovery Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

Revision history**Document revision history**

Date	Revision	Changes
3-Sep-2021	1.0	First release

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