

40V 35A N-Channel Enhancement Mode Power MOSFET

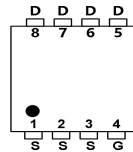
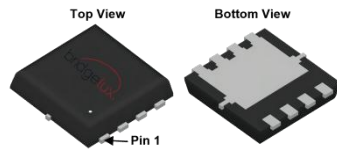
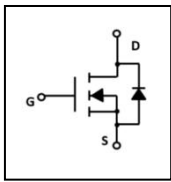
Features

- $R_{DS(on)} \leq 7.1m\Omega$ @ $V_{GS}=10V$
- Advanced trench technology
- Excellent $R_{DS(on)}$ and Low Gate Charge
- Lead free product is acquired

Application

- Load Switch
- PWM Application
- Power management

SYMBOL



PDFN3.3*3.3

ASSEMBLY MESSAGE

Product Name	Package	Packaging
BXT071N04E	PDFN3.3*3.3	Reel

ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Rating	Unit
		PDFN3.3*3.3	
Drain-Source Voltage	V_{DSS}	40	V
Drain Current	I_D	Continuous ($T_C = 25^\circ C$)	35
		Continuous ($T_C = 100^\circ C$)	23
Drain Current	I_{DM}	Pulsed (Note1)	140
Single Pulsed Avalanche Energy	EAS	19	mJ
Gate-Source Voltage	V_{GSS}	± 20	V
Power Dissipation	$T_C = 25^\circ C$	P_D	42
Maximum Junction Temperature	T_J	150	$^\circ C$
Storage Temperature Range	T_{STG}	-55 to 150	$^\circ C$

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

THERMAL CHARACTERISTICS

Parameter	Symbol	Max.	Unit
		PDFN3.3*3.3	
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2.97	$^\circ C / W$

ELECTRICAL CHARACTERISTICS ($T_J=25^{\circ}\text{C}$, unless otherwise Noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	40			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=40V, V_{GS}=0V$			1	μA
Gate-Body Leakage Current, Forward	I_{GSS}	$V_{GS}=20V$			100	nA
Gate-Body Leakage Current, Reverse		$V_{GS}=-20V$			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.5	2.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=20A$		5.5	7.1	$m\Omega$
		$V_{GS}=4.5V, I_D=10A$		7	8	$m\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{DS}=20V, V_{GS}=0V,$ $f=1.0MHz$		1425		pF
Output Capacitance	C_{OSS}			221		pF
Reverse Transfer Capacitance	C_{RSS}			77		pF
SWITCHING PARAMETERS						
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD}=15V, I_D=20A, V_{GS}$ $= 10V, R_G=5\Omega$		11		ns
Turn-ON Rise Time	t_R			7		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			25		ns
Turn-OFF Fall-Time	t_F			5		ns
Total Gate Charge(Note2)	Q_G	$V_{DS}=20V, V_{GS}=10V,$ $I_D=20A$		18		nC
Gate Source Charge	Q_{GS}			12		nC
Gate Drain Charge	Q_{GD}			6		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=20A, V_{GS}=0V$			1.4	V
Diode Continuous Forward Current	I_S				35	A
Maximum Pulsed Drain to Source Diode Forward Current	I_{SM}				140	A

Note: 2. Essentially independent of operating temperature

TYPICAL CHARACTERISTICS

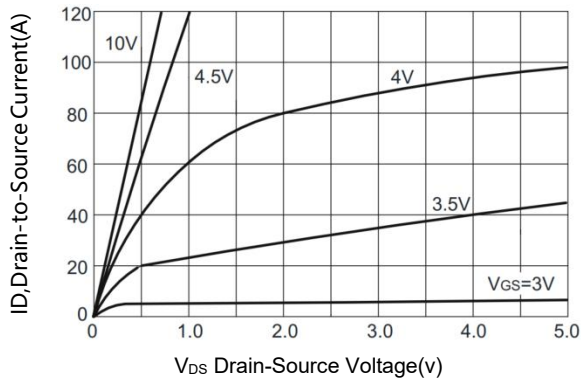


Figure1. Typical Output Characteristics

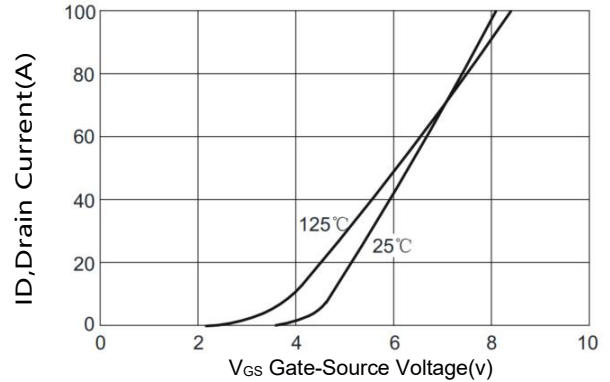


Figure2. Typical Transfer Characteristics

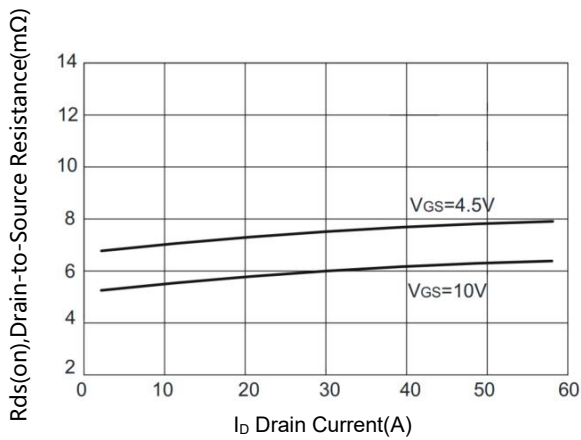


Figure3. On-Resistance versus Drain Current

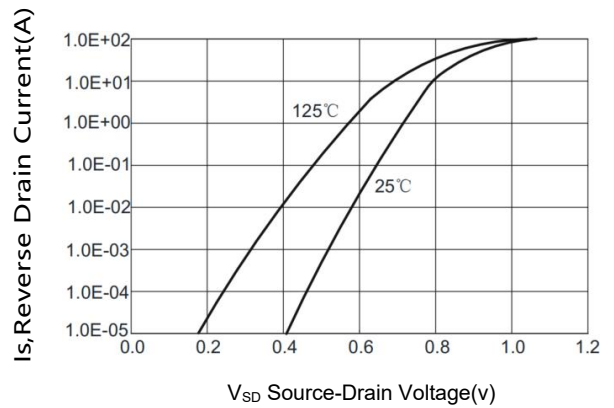


Figure4. Diode forward voltage versus Current

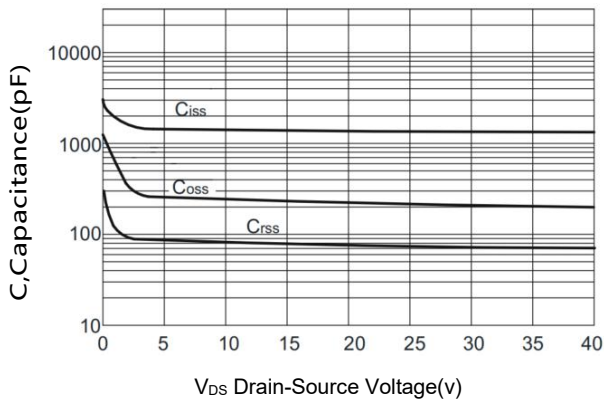


Figure5. Typical Capacitance versus VDS

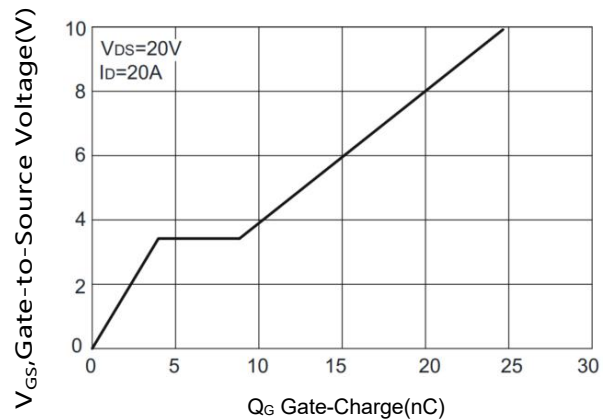


Figure6. Typical Gate Charge versus VGS

TYPICAL CHARACTERISTICS(Cont.)

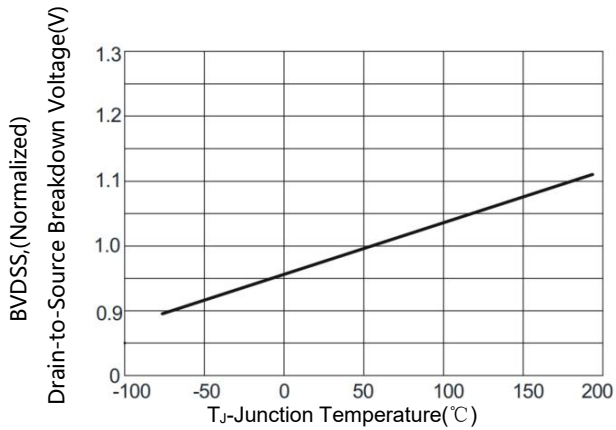


Figure7. BV_{DSS} Variation with Temperature

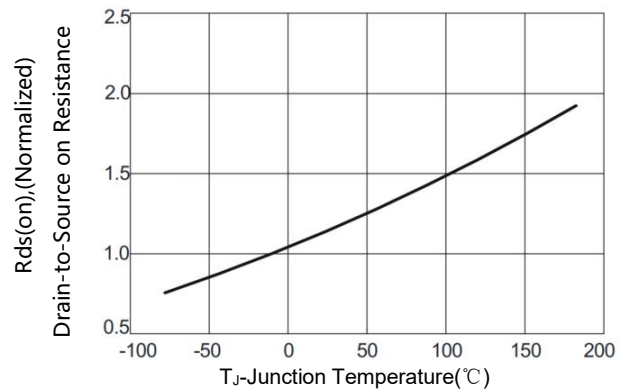


Figure8. On-Resistance Variation with Temperature

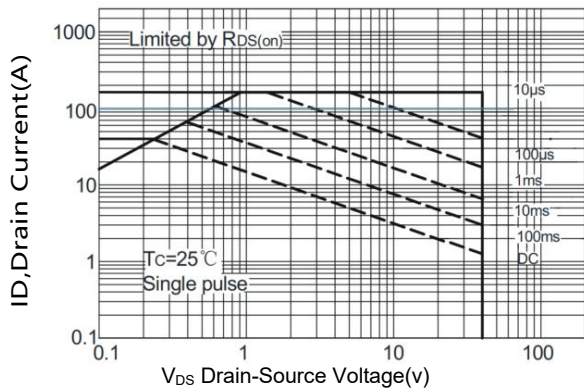


Figure9. Maximum Safe Operating Area

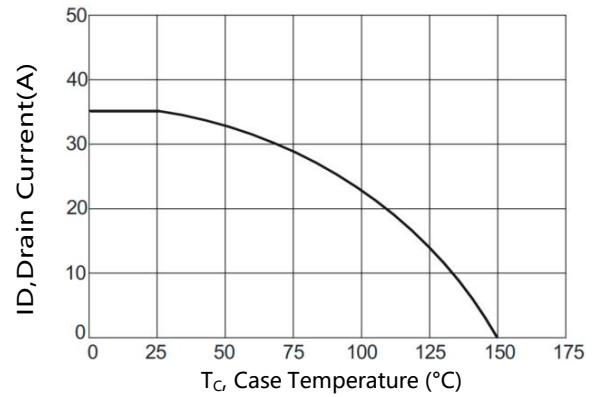
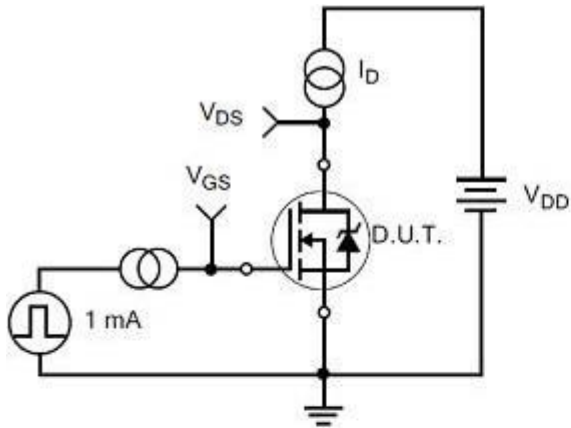
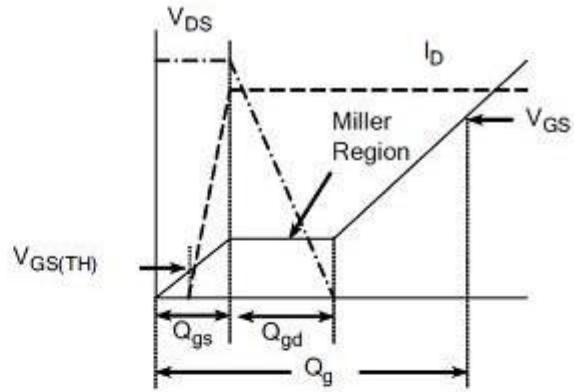


Figure10. Maximum Continuous Drain Current versus Case Temperature

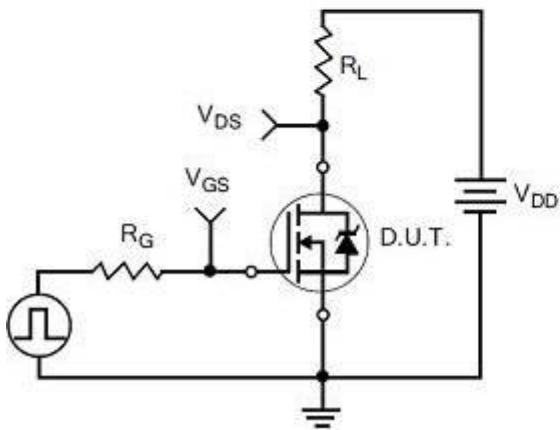
TEST CIRCUITS AND WAVEFORMS



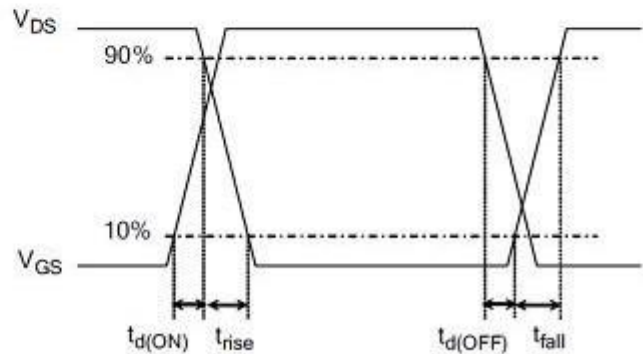
Gate Charge Test Circuit



Gate Charge Waveform

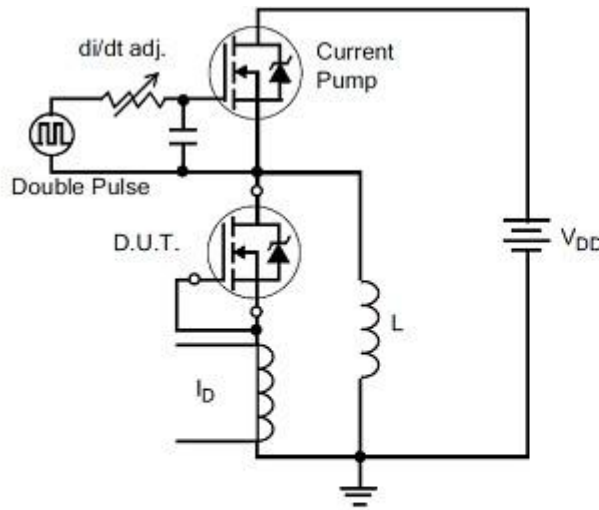


Resistive Switching Test Circuit

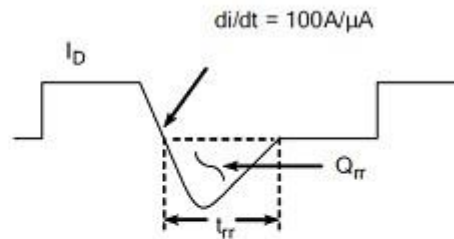


Resistive Switching Waveforms

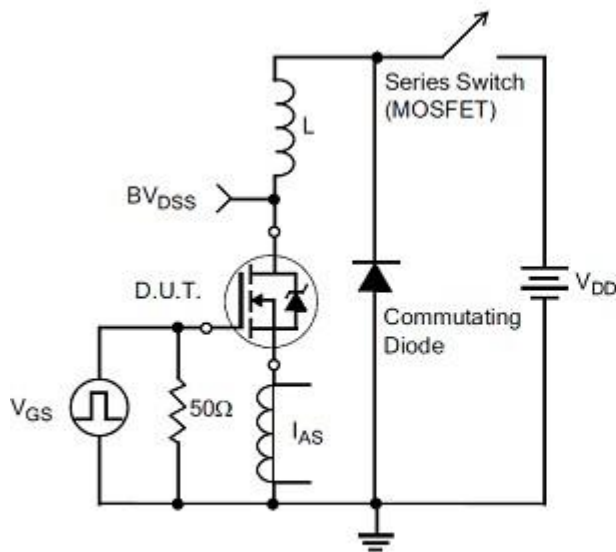
TEST CIRCUITS AND WAVEFORMS(Cont.)



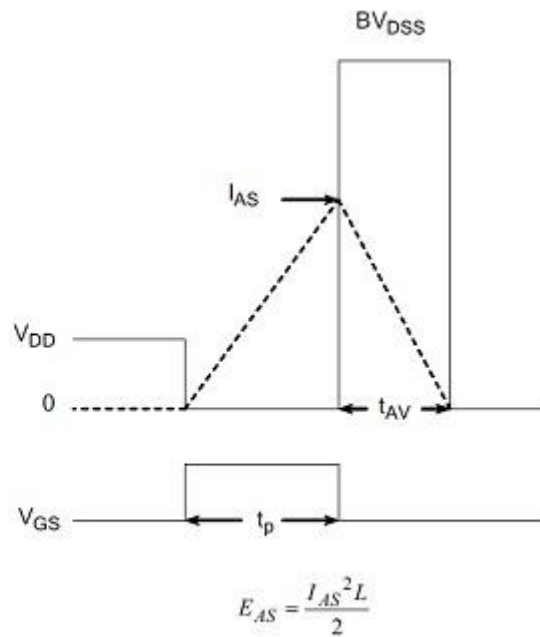
Diode Reverse Recovery Test Circuit



Diode Reverse Recovery Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

Revision history**Document revision history**

Date	Revision	Changes
22-Nov-2021	1.0	First release

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